



CVR COLLEGE OF ENGINEERING
An UGC Autonomous Institution - Affiliated to JNTUH
Vastunagar, Mangalpalli (V), Ibrahimpatan (M), R.R. Dist – 501 510.

EXAMINATION BRANCH

EXAMINATION NOTIFICATION

M.TECH - I YEAR I SEM - MAIN EXAMS - April,2015 (R13) 2014 Admitted Batch.
AND
M.TECH.- I YEAR I SEM SUPPLE. EXAMS – April, 2015 (R11) – 2011 & 2012/(R13) - 2013 Admitted Batches)
M.TECH.- I YEAR II SEM SUPPLE. EXAMS – April, 2015 (R11 – 2011 & 2012/(R13) - 2013 Admitted Batches)

The students who wish to appear for the above examinations commencing from **13-04-2015** are hereby informed to note the dates of exams as per the time table displayed on the notice boards. The detailed dates of exams are as follows.

For M.Tech. (R13 - 2014 Admitted Batch)

M.Tech I Year I Sem Main - 13-04-2015 to 24-04-2015

For M.Tech. (R11 - 2011 & 2012 Admitted Batches & R13 - 2013 Admitted Batch)

M.Tech I Year I Sem Supple - 13-04-2015 to 24-04-2015

M.Tech I Year II Sem Supple - 15-04-2015 to 27-04-2015

The following are the details of payment of examination registration fee.

Without Late Fee	:	27.03.2015
With Late Fee Of Rs.100/-	:	31.03.2015
With Late Fee Of Rs.1000/-	:	10.04.2015 (i.e. up to 2 days before the commencement of main exams)

EXAMINATION FEE :	
M.Tech I-I Sem . Main (One Subject)	Rs. 500/-
M.Tech I-I Sem . Main (Two Subjects)	Rs. 700/-
M.Tech I-I Sem . Main (Three & Above Or All Subjects)	Rs. 900/-
<i>Students appearing M.Tech. 1st Supple. Exams (Each Paper)</i>	Rs. 700/-
<i>Students appearing M.Tech. 2nd Supple. Exams (Each Paper)</i>	Rs. 900/-
<i>The revised rates of Exam Fee to be collected from the academic year 2014-15 is displayed on the Notice Board.</i>	
<i>The count of Supplementary exam that the student is appearing as on April 2015 is also displayed on the Notice Board governing batch wise (i.e., 2011, 2012, 2013 & 2014 admitted batches).</i>	

Note :

1. Hall Tickets will be issued on the day **Main/Supple.** examinations only to the eligible candidates who fulfilled the academic regulations.
2. The Condonation fee is Rs.300/- per candidate.
3. Students who have registered for the main examination but not satisfied the attendance criterion/detained by the college will not be issued hall-ticket even if they have registered for the examination.


PRINCIPAL**DATE: 13.03.2015****Copy to the:**

Director,
 Dean, Academics,
 Vice-Principal & Prof. I/c of Exams
 Controller of Exams/ Asst. Controller of Exams
 H.O.D's (M.Tech. - CE/EEE/ECE/CSE)
 A.O./A.R Exams,
 P.A. to Principal,
 Results Processing Section,
 Chairman - for favour of information
 Hon'. Secretary - for favour of information



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EXAMINATION BRANCH

TIME TABLE OF M.Tech. I Year I Sem. MAIN Exams - 2014 Admitted Batch - April, 2015

TIME: 10.00 A.M TO 1.00 P.M

BRANCH	CIVIL	EEE	ECE			CSE
<u>COURSES</u> Date & Day	(STRUCTURAL ENGINEERING)	(ELECTRICAL POWER ENGINEERING)	(VLSI SYSTEM DESIGN)	(EMBEDDED SYSTEMS)	(WIRELESS & MOBILE COMMUNICATIONS)	(COMPUTER SCIENCE & ENGINEERING)
13.04.15 (Monday)	Theory of Elasticity and Plasticity	Advanced Power System Analysis	VLSI Technology & Design	Embedded System Design	Advanced Data Communications	Advanced Data Structures & Algorithms
16.04.15 (Thursday)	Computer Oriented Numerical Methods	Advanced Power System Protection	Micro Controllers for Embedded System Design (Elective – II) (Common to VLSI & ES)	Microcontrollers for Embedded Systems Design (Common to VLSI & ES)	TCP IP / ATM Networks	Computer System Design
18.04.15 (Saturday)	Structural Dynamics	Renewable Energy Sources	CPLD & FPGA Architectures and Applications	Embedded Real Time Operating Systems	Advanced Digital Signal Processing	Software Architecture & Design Pattern (Elective – II)
21.04.15 (Tuesday)	Advanced Reinforced Concrete Design	Modern Control Theory	CMOS Digital Integrated Circuit Design	Embedded C	Image & Video Processing	Distributed Systems
23.04.15 (Thursday)	Advanced Structural Analysis	EHVAC Transmission (Elective – I)	Digital System Design (Elective – I)	Advanced Computer Architecture (Elective – I)	Detection and Estimation Theory (Elective – I)	Software Process & Project Management (Elective – I)
25.04.15 (Saturday)	Advanced Concrete Technology	High Voltage DC Transmission (Elective – II)	CMOS Analog Integrated Circuit Design	Advanced Operating Systems (Elective – II) (Common to ES & CSE)	Satellite Communications (Elective – II)	Advanced Operating Systems (Common to ES & CSE)

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

Date: 13-03-15

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EXAMINATION BRANCH

Time Table of M.Tech. I Year I Sem. Supplementary Exams - (R13 - 2013 Admitted Batch) - April, 2015

TIME: 10.00 A.M TO 01.00 P.M

<u>Branch</u>	EEE	ECE			CSE	
<u>COURSES → DATE & DAY</u>	ELECTRICAL POWER ENGINEERING	VLSI SYSTEM DESIGN	EMBEDDED SYSTEMS	WIRELESS & MOBILE COMMUNICATIONS	COMPUTER SCIENCE & ENGINEERING	COMPUTER SCIENCE
13.04.15 (Monday)	Advanced Power System Analysis	CMOS Analog Integrated Circuit Design	Embedded Real Time Operating Systems	Spread Spectrum communication (Elective – II)	Advanced Data Structures & Algorithms <i>(Common to CSE, CS)</i>	Advanced Data Structures & Algorithms <i>(Common to CSE, CS)</i>
16.04.15 (Thursday)	Advanced Power System Protection	CPLD & FPGA Architectures and Applications	Embedded System Design	Advanced Data Communications	Computer System Design <i>(Common to CSE, CS)</i>	Computer System Design <i>(Common to CSE, CS)</i>
18.04.15 (Saturday)	Modern Control Theory	Advanced Operating Systems (Elective – II) <i>(Common to VLSI, ES)</i>	Advanced Operating Systems (Elective – II) <i>(Common to VLSI, ES,)</i>	Image & Video Processing	Advanced Operating Systems <i>(Common to CSE, CS)</i>	Advanced Operating Systems <i>(Common to CSE, CS)</i>
21.04.15 (Tuesday)	High Voltage DC Transmission (Elective – II)	CMOS Digital Integrated Circuit Design	Embedded C	TCP IP / ATM Networks	Software Process & Project Management (Elective – I) <i>(Common to CSE, CS)</i>	Software Process & Project Management <i>(Common to CSE, CS)</i>
23.04.15 (Thursday)	Renewable Energy Systems	Digital System Design (Elective – I)	Advanced Computer Architecture (Elective – I)	Detection and Estimation Theory (Elective – I)	Software Architecture & Design Patterns (Elective – II) <i>(Common to CSE, CS)</i>	Software Architecture & Design Patterns (Elective – II) <i>(Common to CSE, CS)</i>
25.04.15 (Saturday)	EHVAC Transmission (Elective – I)	VLSI Technology & Design	Microcontrollers for Embedded System Design	Advanced Digital Signal Processing	Distributed Systems <i>(Common to CSE, CS)</i>	Distributed Systems (Elective – I) <i>(Common to CSE, CS)</i>

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EXAMINATION BRANCH

Time -Table of M.Tech. I Year I Sem. Supplementary Exams - (R11 - 2011 & 2012 Admitted Batch) - April, 2015

TIME: 10.00 A.M TO 1.00 P.M

<u>Branch</u>	EEE	ECE			CSE	
<u>COURSES → DATE & DAY</u>	ELECTRICAL POWER ENGINEERING	VLSI SYSTEM DESIGN	EMBEDDED SYSTEMS	WIRELESS & MOBILE COMMUNICATIONS	COMPUTER SCIENCE & ENGINEERING	COMPUTER SCIENCE
13.04.15 (Monday)	Power System Dynamics	Micro Controllers for Embedded System Design <i>(Common to VLSI & ES)</i>	Micro Controllers for Embedded System Design <i>(Common to VLSI & ES)</i>	Advanced Data Communications	Advanced Problem Solving <i>(Common to CSE & CS)</i>	Advanced Problem Solving <i>(Common to CSE & CS)</i>
16.04.15 (Thursday)	High Voltage DC Transmission	CPLD & FPGA Architecture & Applications <i>(Common to VLSI & WMC)</i>	Embedded Real Time Operating Systems	<u>Elective - I:</u> CPLD & FPGA Architecture and Applications <i>(Common to VLSI & WMC)</i>	Computer System Design <i>(Common to CSE & CS)</i>	Computer System Design <i>(Common to CSE & CS)</i>
				R.F Circuit Design		
18.04.15 (Saturday)	Modern Control Theory	<u>Elective - II:</u> Network Security & Cryptography	<u>Elective - II:</u> Advanced Operating Systems	Advanced Digital Signal Processing <i>(Common to VLSI & WMC)</i>	Embedded Systems	Software Design and Engineering
		Advanced Digital Signal Processing <i>(Common to VLSI & WMC)</i>				
21.04.15 (Tuesday)	Microprocessors & Micro Controllers	Algorithms for VLSI Design Automation	Advanced Computer Networks	Image & Video Processing	Java and Web Technologies <i>(Common to CSE & CS)</i>	Java and Web Technologies <i>(Common to CSE & CS)</i>
23.04.15 (Thursday)	<u>Elective - I:</u> High Voltage Engg. & Insulation Co-ordination	<u>Elective - I:</u> Digital System Design	<u>Elective - I:</u> Advanced Computer Architecture	Internetworking	<u>Elective - I:</u> Object Oriented Modeling <i>(Common to CSE & CS)</i>	<u>Elective - I:</u> Object Oriented Modeling <i>(Common to CSE & CS)</i>
		Hardware Software Co-Design			Software Architecture & Design Patterns	Software Architecture & Design Patterns
25.04.15 (Saturday)	<u>Elective - II:</u> Energy Conversion Systems	VLSI Technology & Design <i>(Common to VLSI & ES)</i>	VLSI Technology & Design <i>(Common to VLSI & ES)</i>	<u>Elective - II:</u> Satellite Communications	<u>Elective - II:</u> Advanced Compiler Design	<u>Elective - II:</u> Image Processing
				Voice over Internet Protocol		Image Processing & Pattern Recognition

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Time Table of M.Tech. I Year II Sem. Supplementary Exams - (R13 - 2013 Admitted Batch) - April, 2015

TIME: 10.00 A.M TO 1.00 P.M

BRANCH	EPE	ECE			CSE	
Date & Day	(ELECTRICAL POWER ENGINEERING)	(VLSI SYSTEM DESIGN)	(EMBEDDED SYSTEMS)	(WIRELESS & MOBILE COMMUNICATIONS)	(COMPUTER SCIENCE & ENGINEERING)	(COMPUTER SCIENCE)
15-04-15 (Wednesday)	Power System Dynamics	Low Power VLSI Design	Hardware Software Co-Design	Coding Theory & Techniques	Advanced Network Programming (Common to CSE & CS)	Advanced Network Programming (Common to CSE & CS)
17-04-15 (Friday)	Flexible AC Transmission Systems	CAD for VLSI Circuits	Wireless Communications & Networks (Elective – III) (Common to ES & WMC)	Wireless Communication & Networks (Common to ES & WMC)	Advanced Databases (Common to CSE & CS)	Advanced Databases (Common to CSE & CS)
20-04-15 (Monday)	Power System Operation and Deregulation	CMOS Mixed Signal Circuit Design	Embedded Networking	Optical Communications Technology	Web Services and Service Oriented Architecture (Common to CSE & CS)	Web Services and Service Oriented Architecture (Common to CSE & CS)
22-04-15 (Wednesday)	AI Techniques in Electrical Power Engineering	Design for Testability	CPLD & FPGA Architectures and Applications	Telecommunication Switching Systems & Networks	Wireless Networks and Mobile Computing (Elective – IV) (Common to CSE & CS)	Wireless Networks and Mobile Computing (Common to CSE & CS)
24-04-15 (Friday)	Electric Smart Grid (Elective – III)	Digital Signal Processors & Architectures (Elective – III) (Common to VLSI & ES)	Digital Signal Processors and Architectures (Common to VLSI & ES)	Ad-hoc & Wireless Sensor Networks (Elective – III)	Advanced Data Mining (Elective – III) (Common to CSE & CS)	Advanced Data Mining (Elective – III) (Common to CSE & CS)
27-04-15 (Monday)	Voltage Stability (Elective – IV)	System on Chip Architecture (Elective – IV) (Common to VLSI & ES)	System on Chip Architecture (Elective – IV) (Common to VLSI & ES)	Networks Security & Cryptography (Elective – IV)	Grid & Cloud Computing	Cloud Computing (Elective – IV)

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Time -Table of M.Tech. I Year II Sem. Supplementary Exams - (R11 - 2011 & 2012 Admitted Batch) - April, 2015

TIME: 10.00 A.M TO 1.00 P.M

Branch	EEE	ECE			CSE	
COURSES → DATE & DAY	ELECTRICAL POWER ENGINEERING	VLSI SYSTEM DESIGN	EMBEDDED SYSTEMS	WIRELESS & MOBILE COMMUNICATIONS	COMPUTER SCIENCE & ENGINEERING	COMPUTER SCIENCE
15-04-15 (Wednesday)	Neural and Fuzzy Systems	System on Chip Architecture (Common to VLSI, ES)	<u>Elective – IV :</u> System on Chip Architecture (Common to VLSI, ES)	Coding Theory and Techniques	Distributed Computing (Common to CSE, CS)	Distributed Computing (Common to CSE, CS)
17-04-15 (Friday)	Advanced Power System Protection	<u>Elective – III :</u> Digital Signal Processors and Architectures (Common to VLSI, ES)	Digital Signal Processors and Architectures (Common to VLSI, ES)	Wireless Communication & Networks	Distributed Databases (Common to CSE, CS)	Distributed Databases (Common to CSE, CS)
20-04-15 (Monday)	Power Quality	Embedded Real Time Operating Systems	System Modeling and Simulation	Optical Communications Technology	Advanced Computer Architecture	Advanced Compiler Design
22-04-15 (Wednesday)	Flexible AC Transmission Systems	Design of Fault Tolerant Systems	CPLD and FPGA Architecture and applications	Telecommunication Switching Systems & Networks	Advanced Computer Networks (Common to CSE, CS)	Advanced Computer Networks (Common to CSE, CS)
24-04-15 (Friday)	<u>Elective – III :</u> Digital Signal Processing	CMOS Analog & Mixed Signal Design	<u>Elective – III :</u> Ad-hoc Wireless & Sensor Networks (Common to ES, WMC)	<u>Elective – III :</u> Ad-hoc Wireless & Sensor Networks (Common to ES, WMC)	<u>Elective – III :</u> Web Services (Common to CSE, CS)	<u>Elective – III :</u> Web Services (Common to CSE, CS)
27-04-15 (Monday)	<u>Elective – IV :</u> Power System Operation and Control	<u>Elective – IV :</u> Semi-Conductor Memory Design & Testing	Hardware Software Co- Design	<u>Elective – IV :</u> Network Security & Cryptography	<u>Elective – IV :</u> Information Security (Common to CSE, CS)	<u>Elective – IV :</u> Information Security (Common to CSE, CS)
		Low Power VLSI Design				

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